

Form PTO-447A  
(Rev. 7-98)

Staple to face of Application

U.S. DEPARTMENT OF COMMERCE  
PATENT & TRADEMARK OFFICE

## APPLICATION TRANSFER REQUEST FOR S.N. 09697305

## Section I. TRANSFER REQUEST BY

Name NGUYEN, VINH P Date 2/7/2002

TO: Art Unit 2133 Class/sub 714/724+

FROM: A.U. 2829 Class 324

## REASON:

test semiconductor using logic test

Gatekeeper concurrence:

NTN

## Section IIa. DISPOSITION BY RECEIVING TC

By: ASD A.U. 2109 Date 3/18/02NOT ACCEPTED ☐ Forward to receiving TC Post Classifier

## REASON:

714 class depending requires either ① detecting/correct errors in digital data or ② detect/correcting faults in electrical computers, neither of which is claimed.

## Section IIb. DISPOSITION BY RECEIVING TC POST CLASSIFIER

☒ This dispute was resolved.Forward to TC/AU 2133 Class/Sub 714,733+ Post Classifier HA. Fatchiy Date 3/25/02

The claimed disclosure is directed to a built-in testing circuit for generating test patterns for determining faults/errors in a semiconductor circuit.

☐ This dispute was not resolved, forward to DISPUTE RESOLUTION PANEL

## Post Classifier Assessment:

Gatekeeper concurrence \_\_\_\_\_

Post Classifier \_\_\_\_\_ Date \_\_\_\_\_

## Section III. DISPOSITION BY DISPUTE RESOLUTION PANEL

Date \_\_\_\_\_

## Panel Decision:

Forward To Technology Center/Art Unit \_\_\_\_\_ Class/sub \_\_\_\_\_/\_\_\_\_\_

## REASON:

Panel Member \_\_\_\_\_

Concurring Panel Member \_\_\_\_\_